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37 and 15

Feb. 2005

L30: (22) 28 same 15
 L31: (17) Forward citation search 1
 L32: (5) Forward citation search 2
 L33: (5) 31 and 15
 L34: (34578) (split\$4 wrap\$4 multi doubl\$4 dual side\$2) adj gate\$2
 L35: (316) 34 and 14
 L36: (23) 35 and 15
 L37: (800) 28 and 34
 L38: (96) 37 and 15
 L39: (2) *5537078*.pn
 L41: (78235) 6 8
 L42: (89569) 7 9
 L43: (57102) 41 same 42
 L44: (755) 28 and 43
 L45: (97) 44 and 15
 L46: (1811) 43 same 15
 L47: (28) 28 and 46
 L48: (34) dtmosfet
 L49: (0) 48 with 15
 L50: (5) 48 and 15
 L51: (1418) dynamic adj threshold
 L52: (14) 51 with 15
 L53: (269538) ((((\$3mos metal adj oxide) adj transistor) \$3fet \$2fet)
 L54: (13) 52 and 53

	U	Inventor	Document	Issue P	Title	Current	Current X	Retrieval	S	C	P	Image	Doc	P
1		Dennison	US 675928	2004	2	Methods of forming transistors	438/15	438/153					US 67592	
2		Sin, John	US 588200	1999	1	Polysilicon devices and a method for f	257/34	257/318					US 58820	
3		Hackler, Do	US 200500	2005	2	Sram cell	438/25						US 20050	
4		Parke, Step	US 658013	2003	3	Damascene double gated transistors	257/40	257/407					US 65801	
5		Shepard, J	US 557429	1996	8	Vertical dual gate thin film transistor wi	257/88	257/327					US 55742	
6		Madurawe	US 200500	2005	2	Semiconductor switching devices	365/19						US 20050	
7		Shionoin, Y	US 200500	2005	5	Sense amplifier and electronic apparat	327/85						US 20050	
8		Koyama, J	US 200402	2004	5	Liquid crystal display device and meth	345/87						US 20040	
9		Madurawe	US 200402	2004	3	Semiconductor latches and SRAM dev	438/20						US 20040	
10		Lee, Thom	US 200402	2004	1	Rail stack array of charge storage dev	438/14						US 20040	